



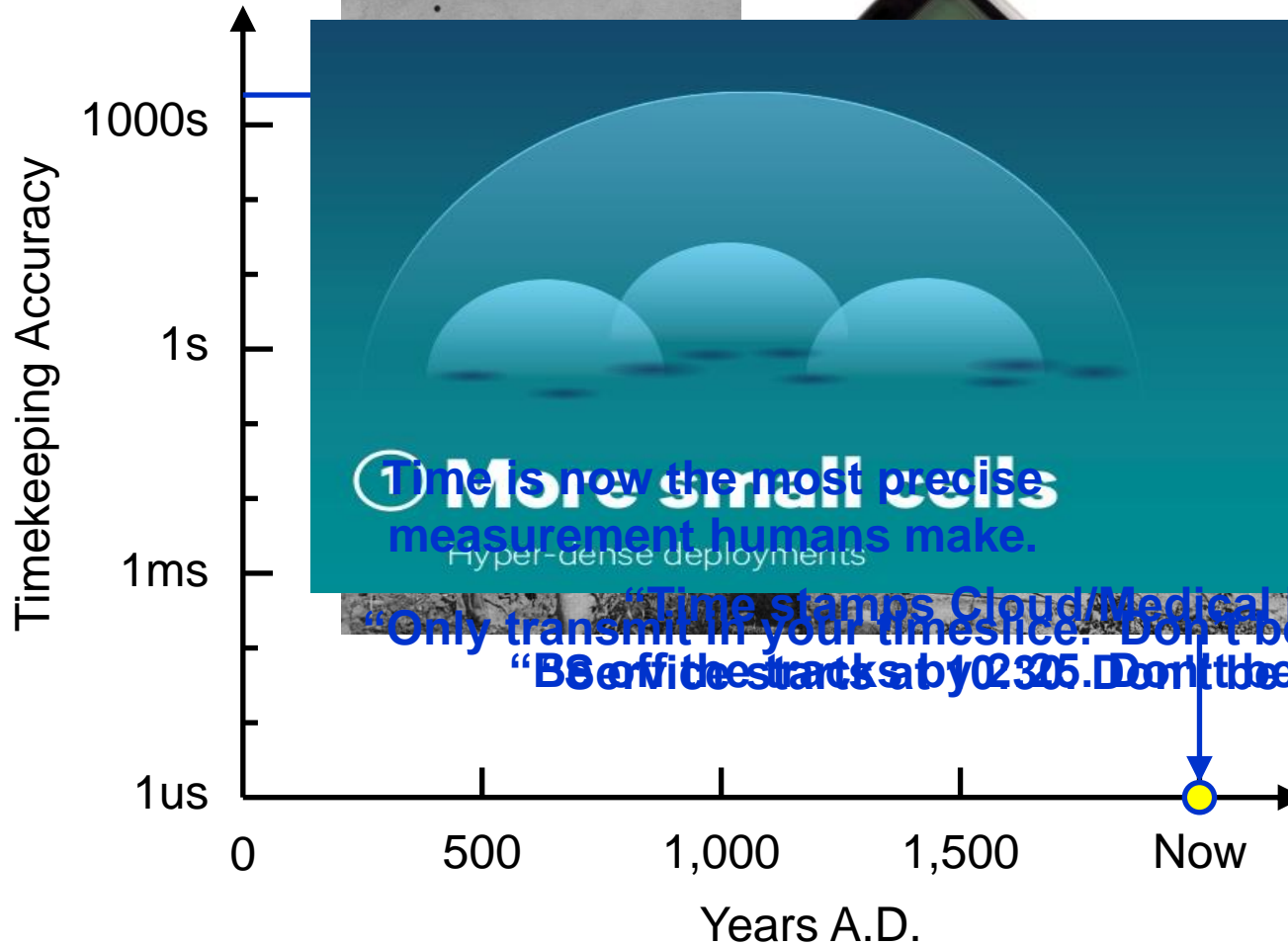
# Industry-Leading Resonator Technology and Product Line

Markus Lutz



The Smart Timing Choice™

# Timekeeping Beyond-Exponential Improvement



# SiTime Locations and Manufacturing

- Fabless analog IC company, founded in 2005, acquired by MegaChips Nov 2014
- 90 employees



● SiTime Locations

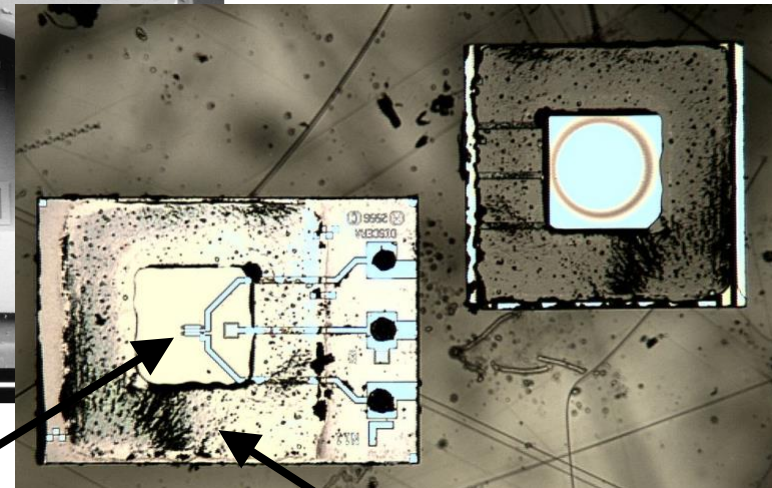
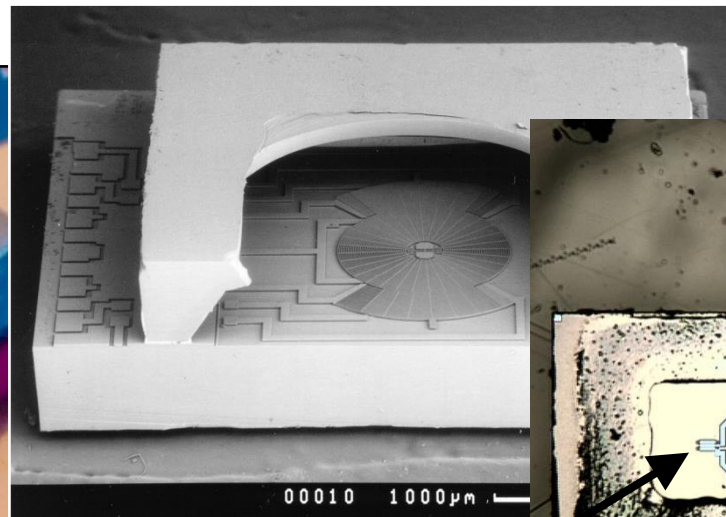
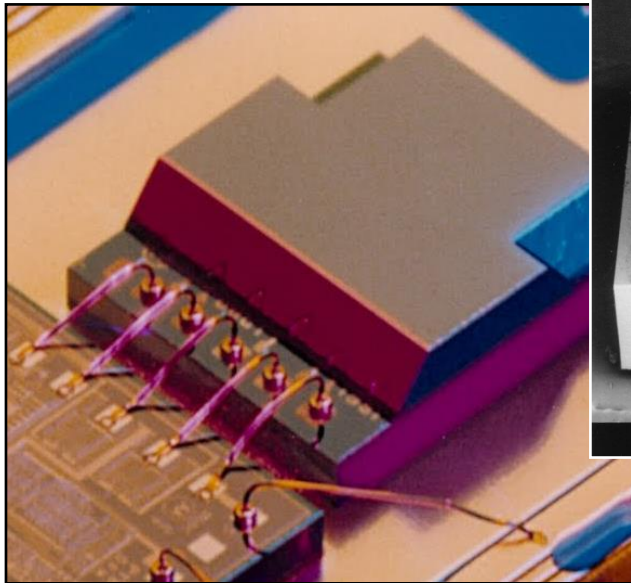
● Direct Sales

● Fabrication (MEMS: Bosch, Tower-Jazz, CMOS: TSMC)

● Assembly and Test (Carsem, UTAC, ASE)

# MEMS Packaging is Key for Success

- Protect the parts in operation – no drift
- Protect the parts during manufacturing – yield
- Protect in back end packaging – manufacturability, low cost
- Small area for packaging – low cost, small size
- Standard materials and processes – manufacturability



Resonator

Large Sealing Area

Common MEMS Packaging Technology before SiTime



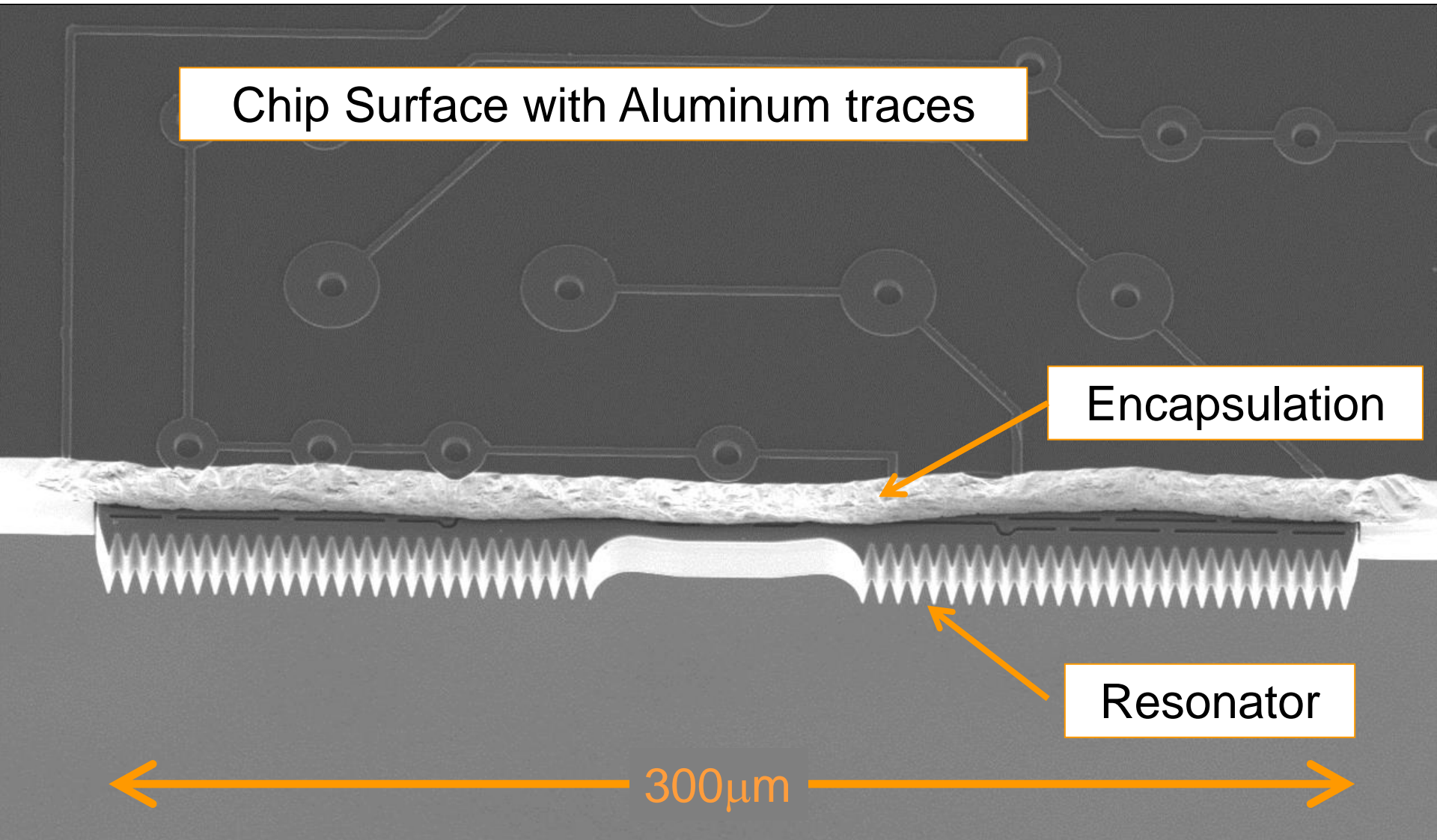
# InChipMEMS™ Cross Section

Chip Surface with Aluminum traces

Encapsulation

Resonator

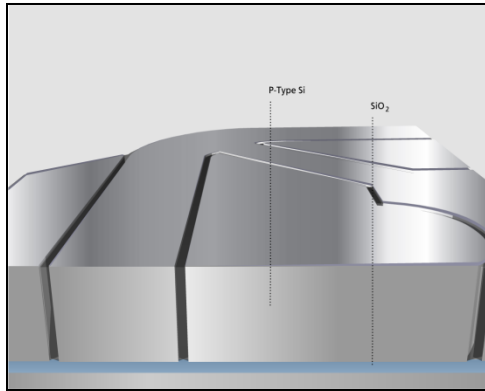
300μm



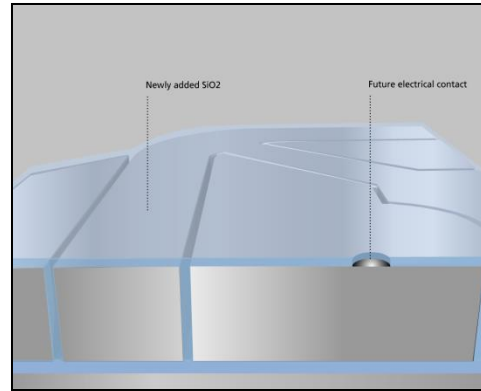
# Advantages of MEMS First™ Process

- Standard CMOS processes and materials
  - Widely available supply chain, low cost
- Industry standard process control and 6 sigma philosophy
  - Manufacturability, high yield, quality, reliability
- High temperature in-process encapsulation to protect MEMS structure
  - Best stability, ultra small drift, small size, reliability, quality
- Standard IC backend (packaging and test)
  - Scalable supply chain, multiple sources, lowest cost
- Monolithic integration CMOS with MEMS possible

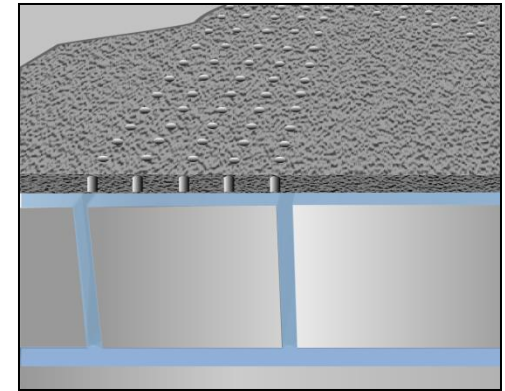
# MEMS Resonator Fabrication



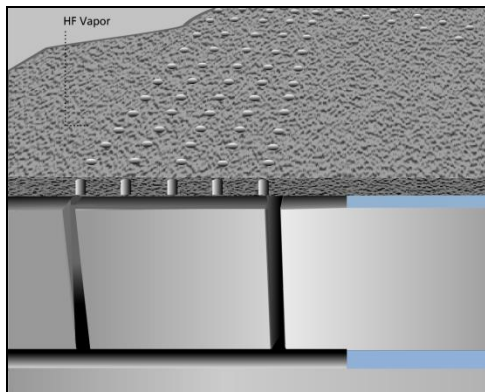
1. Etch SOI wafer



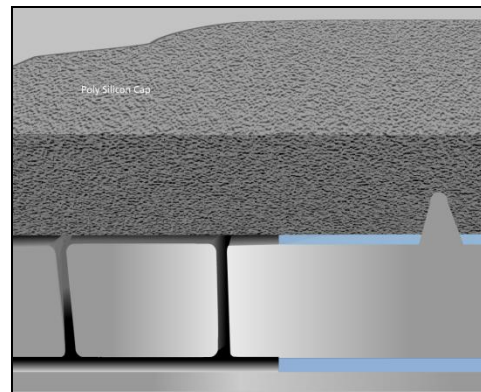
2. Protect under oxide



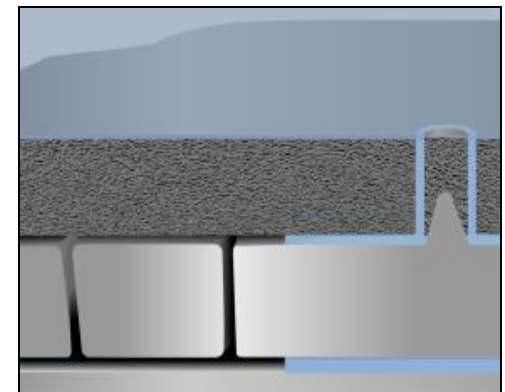
3. Cover and perforate



4. Remove oxide

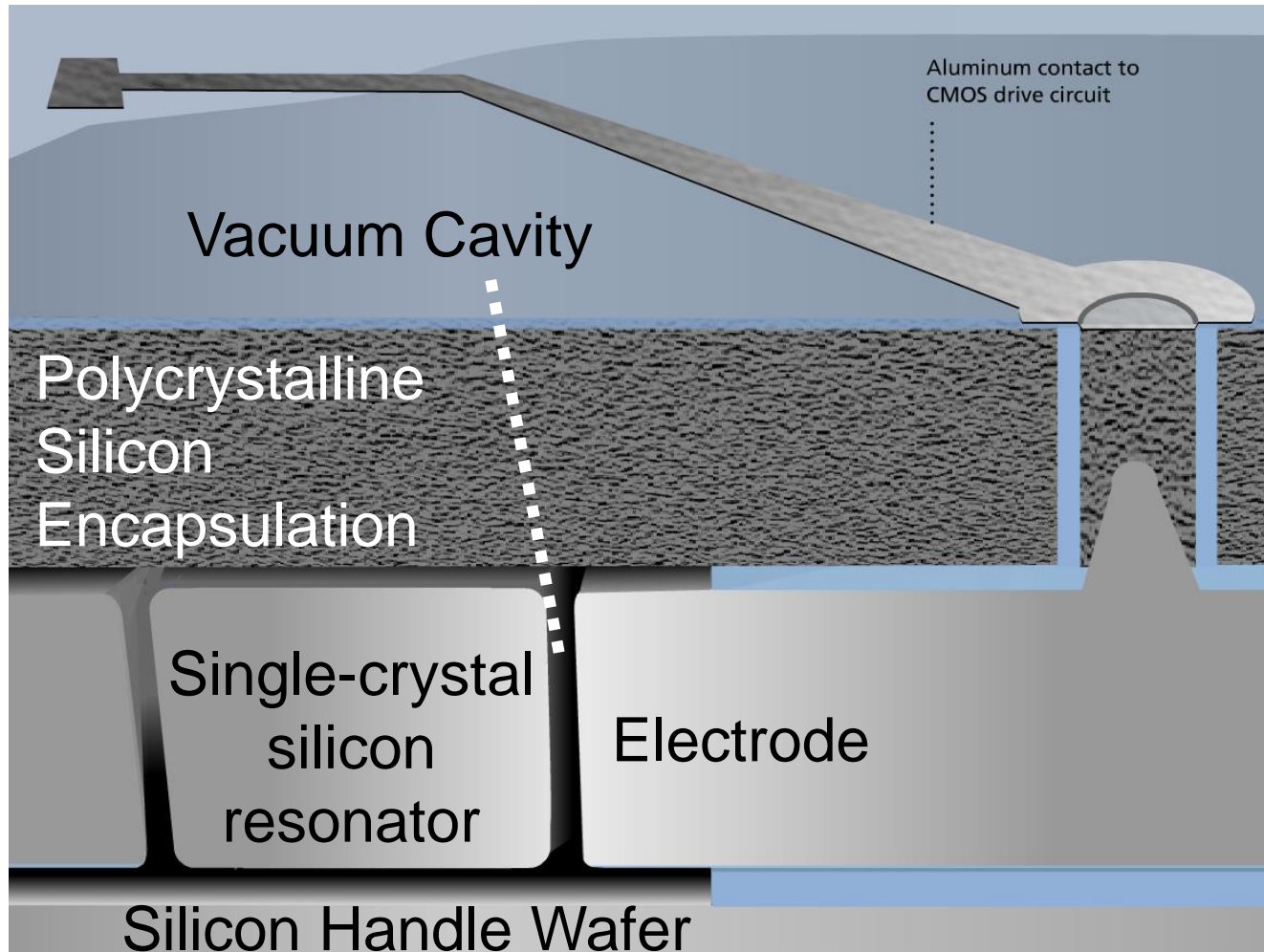


5. Deposit thick silicon



6. Trench Isolation

# MEMS Resonator Fabrication

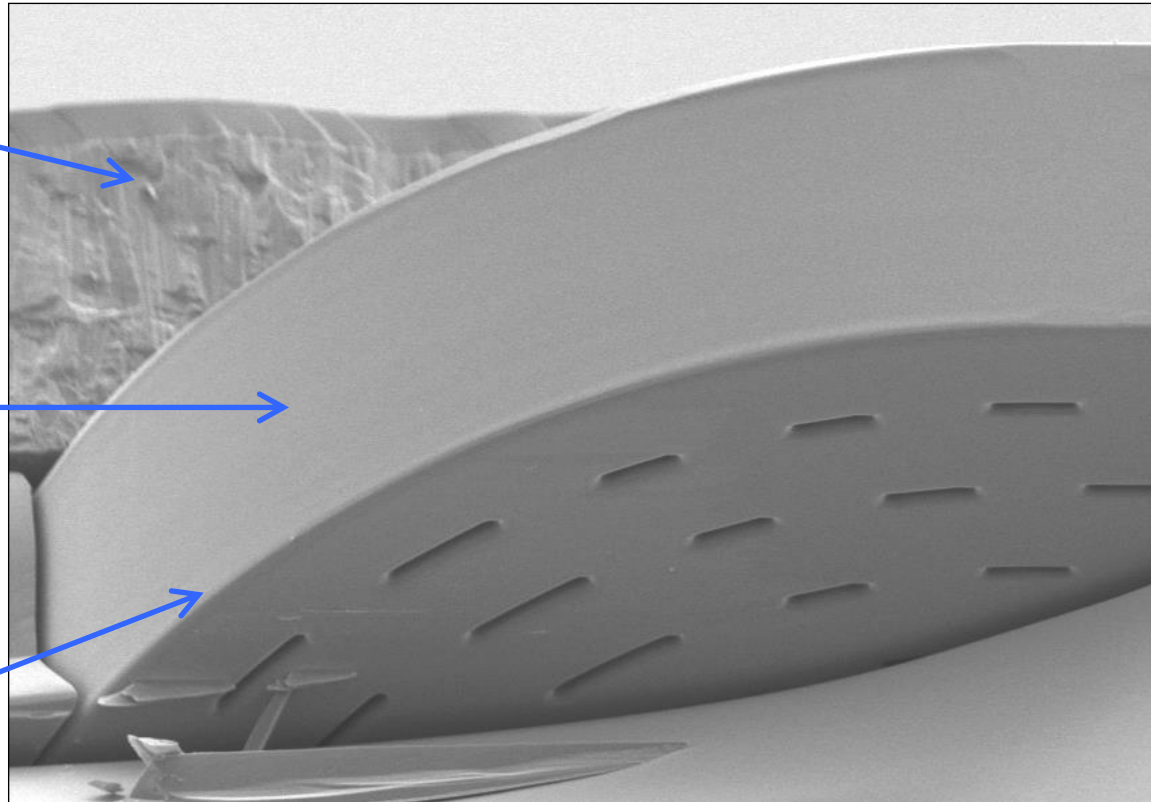


7. Metal and Scratch Mask

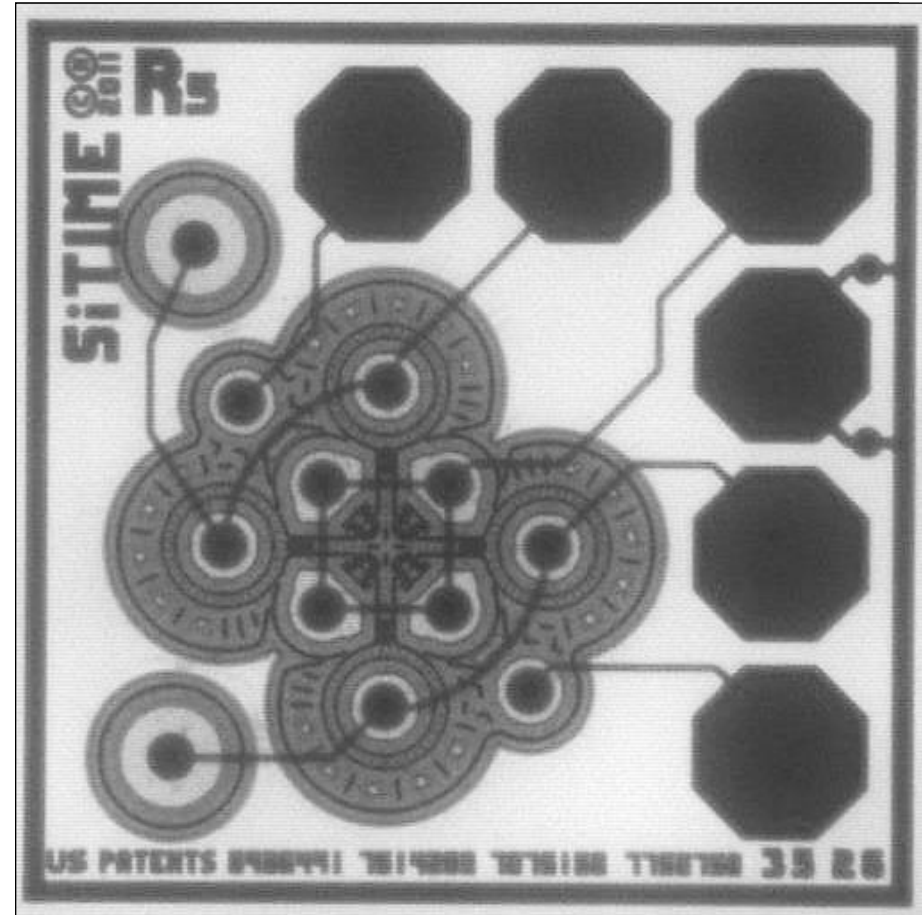
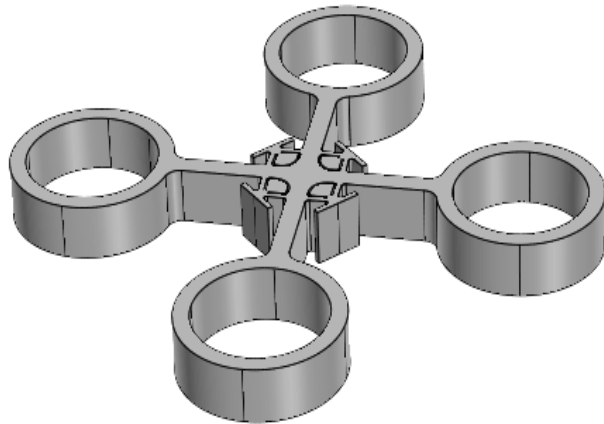


# Single Crystal Silicon Resonator – ZERO Aging

- EpiSeal™ at 1100C  
perfect clean vacuum
- High purity material,  
controlled to ppb levels
- No fatigue
- Single crystal silicon  
with reformed surfaces

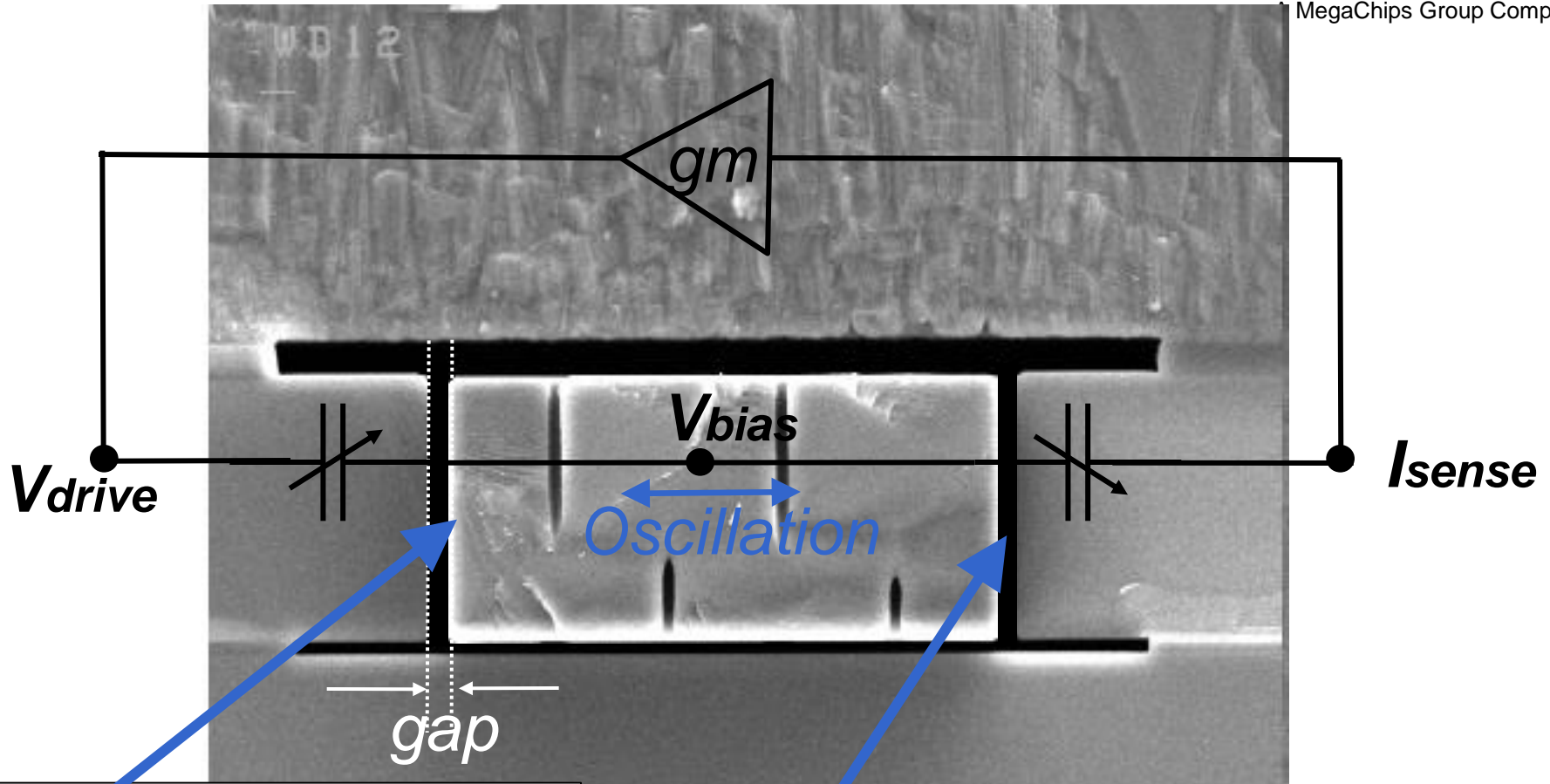


# High Performance Resonators



- $f = 48\text{MHz}$
- $Q = 147\text{k}$
- Smallest size:  $400\mu\text{m} \times 400\mu\text{m}$

# Capacitive Resonator Principle

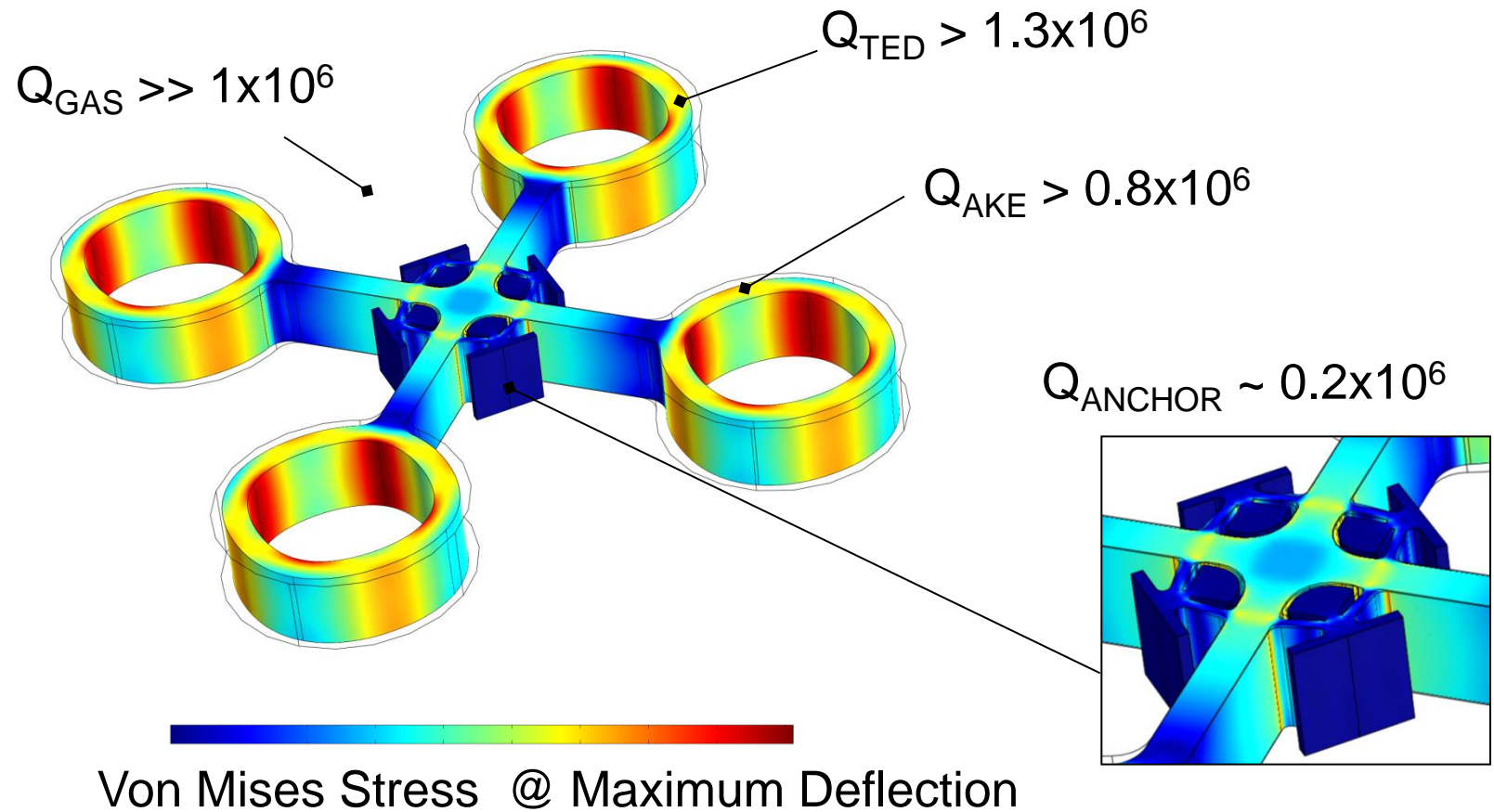


$$Force = \epsilon_0 \frac{area}{gap^2} V_{bias} V_{drive}$$

$$I_{sense} = V_{bias} \frac{dC}{dt} = V_{bias} \frac{d}{dt} \left( \epsilon_0 \frac{area}{gap(t)} \right)$$

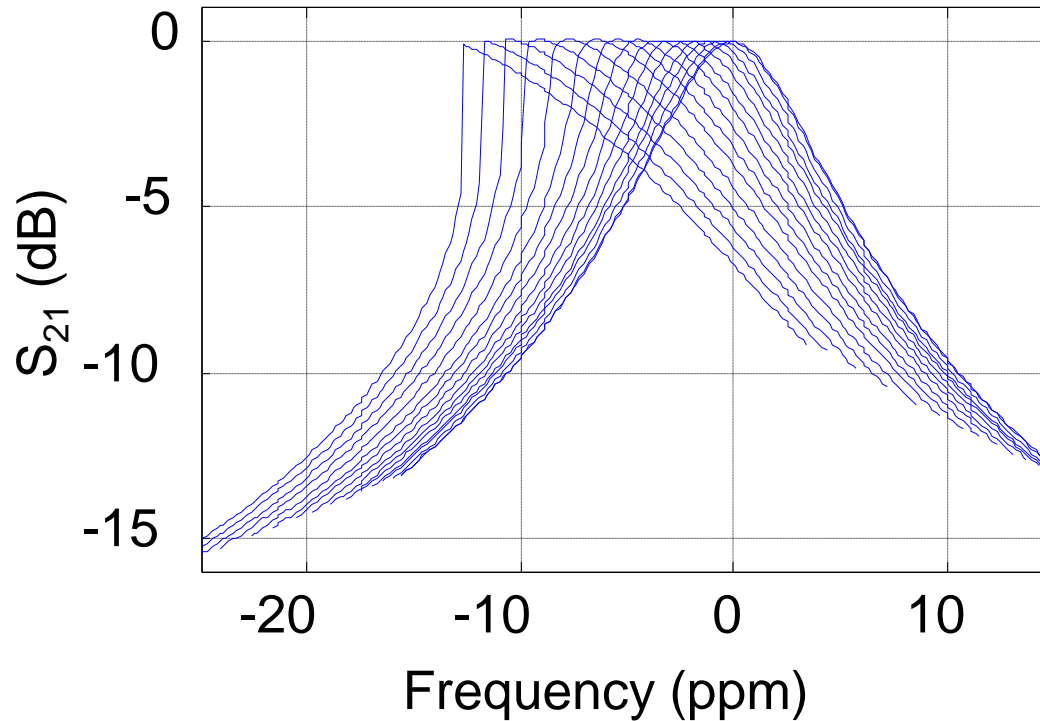
# Optimizing for High Q

$$\frac{1}{Q} = \frac{1}{Q_{GAS}} + \frac{1}{Q_{TED}} + \frac{1}{Q_{AKE}} + \frac{1}{Q_{ANCHOR}} + \frac{1}{Q_{OTHER}}$$



# Optimizing for High Power Handling

- Resonator nonlinearities limit the drive amplitude
- Higher drive amplitude decrease far phase noise
- Overdriving increases near phase noise

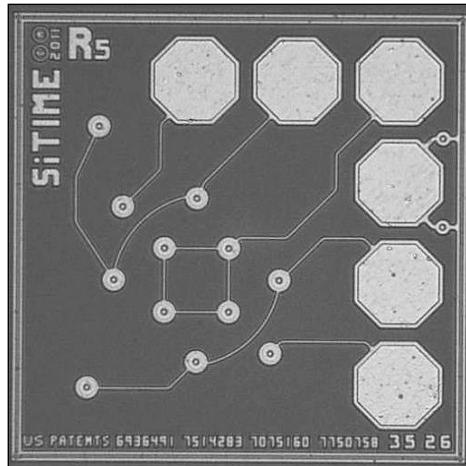




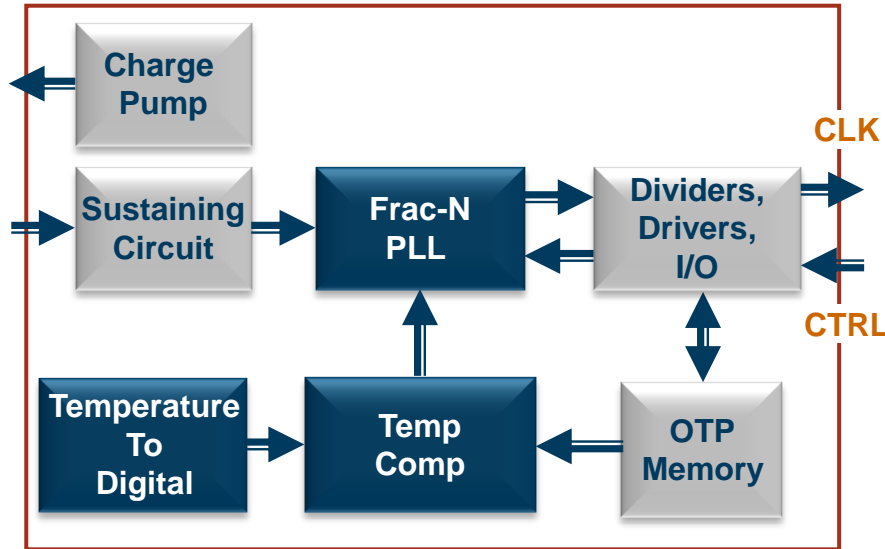
# SiTime's Products

## Best MEMS & Performance & Quality

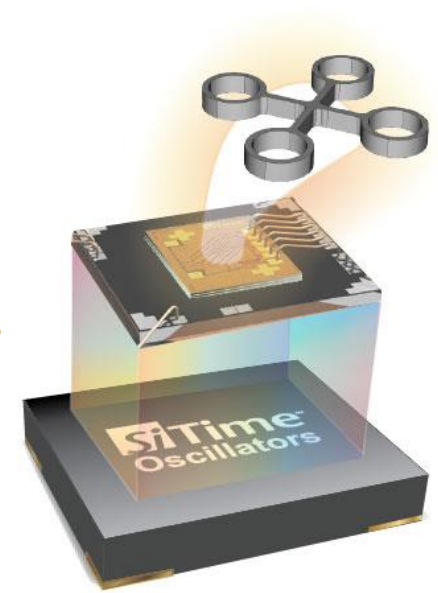
### MEMS Die



### Programmable CMOS IC



### MEMS Oscillator

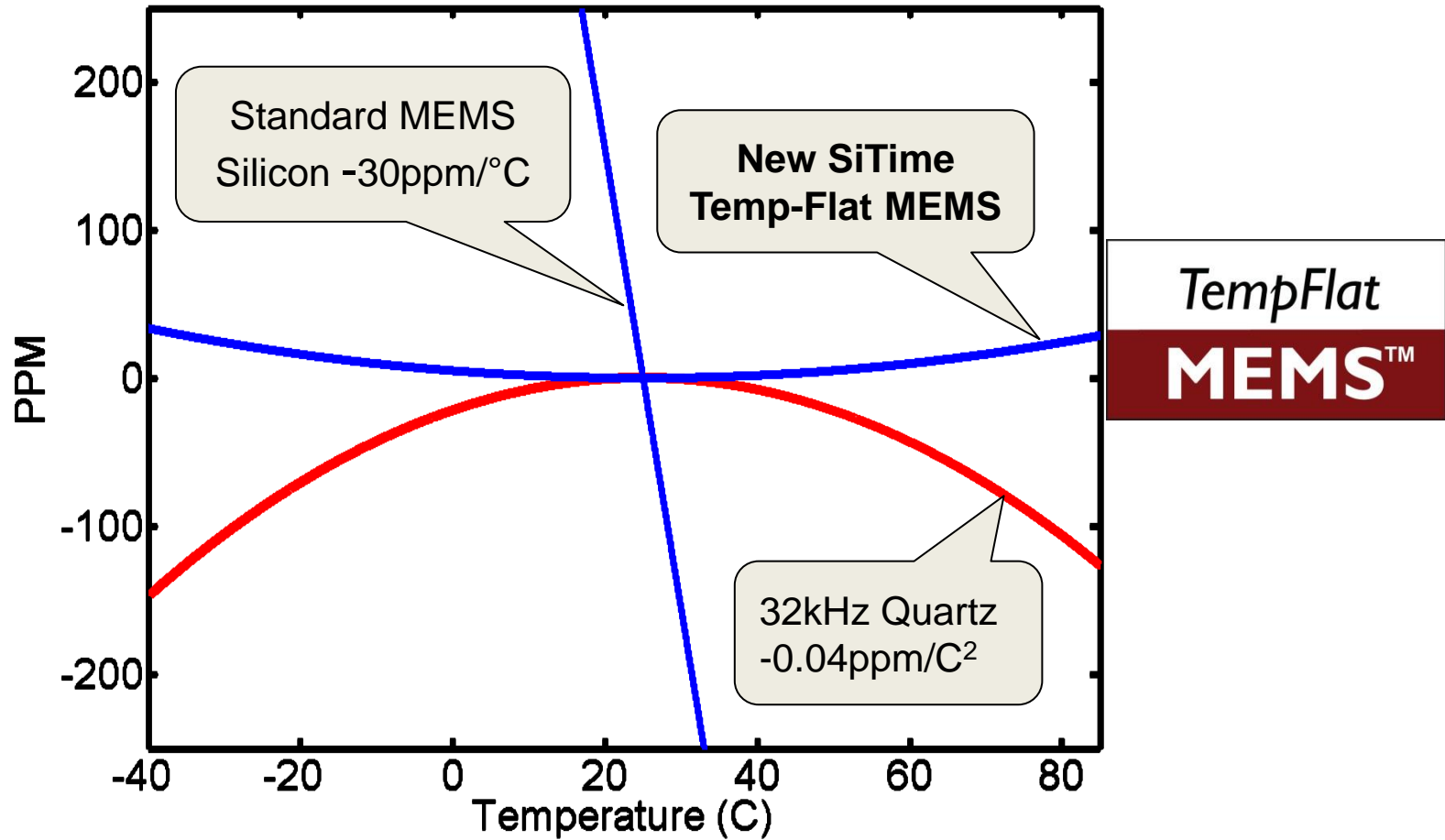


- 0.4 mm x 0.4 mm
- 99% yields
- Vacuum sealed die
- Standard CMOS fab

- Jitter – as low as 300 fs
- Stability - TCXO
- Low Power - 1 $\mu$ A @ 32kHz
- 180nm CMOS – fab TSMC

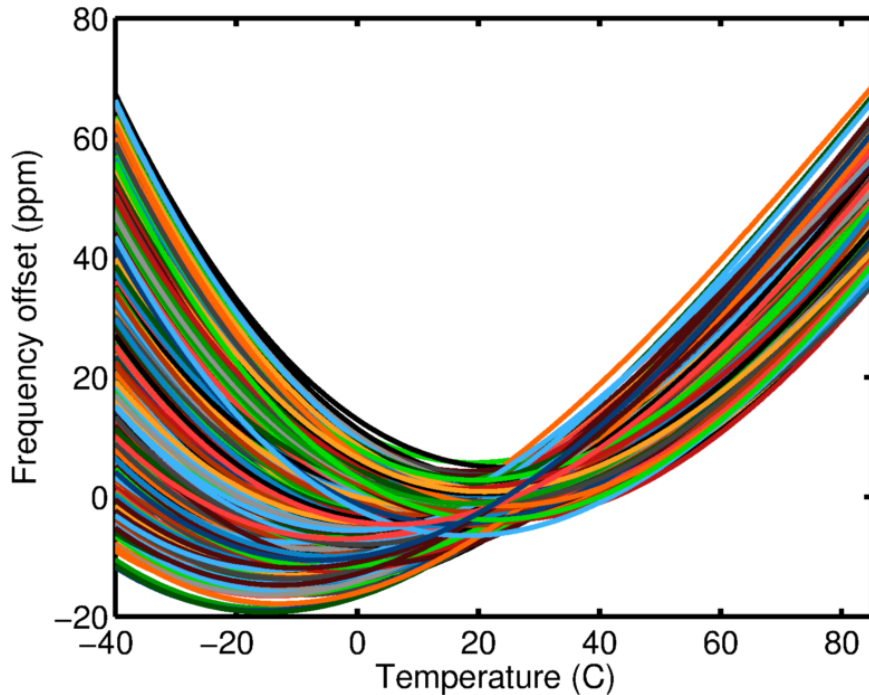
- Plastic packages
- Robust - MSL1
- 99% Yields
- 0.15DPPM, 2Fit

# Temperature Stable MEMS Resonators



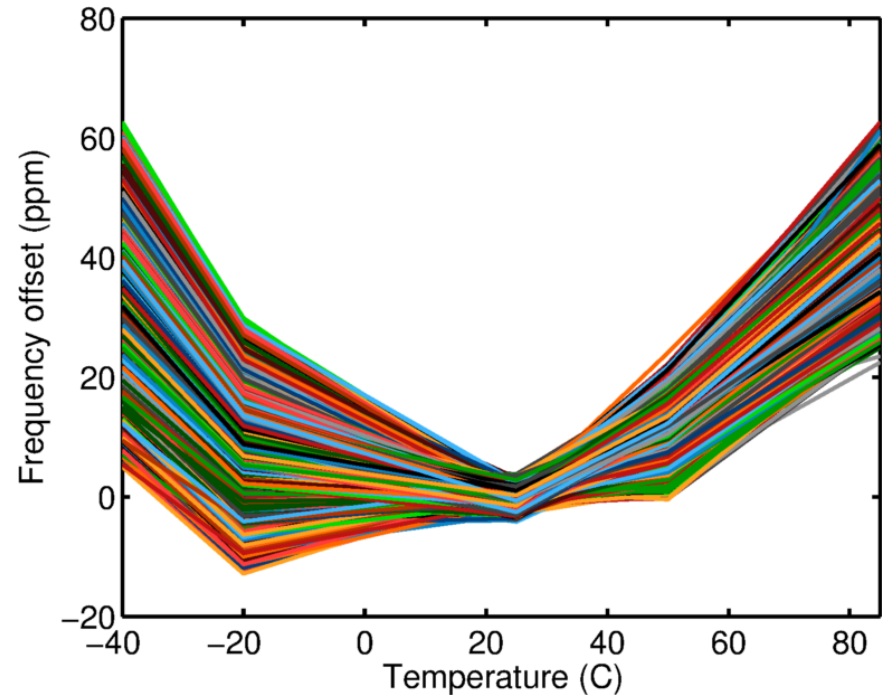
Mechanical temperature stability beats quartz

## Monte Carlo Simulation



Simulation includes MEMS and CMOS variations

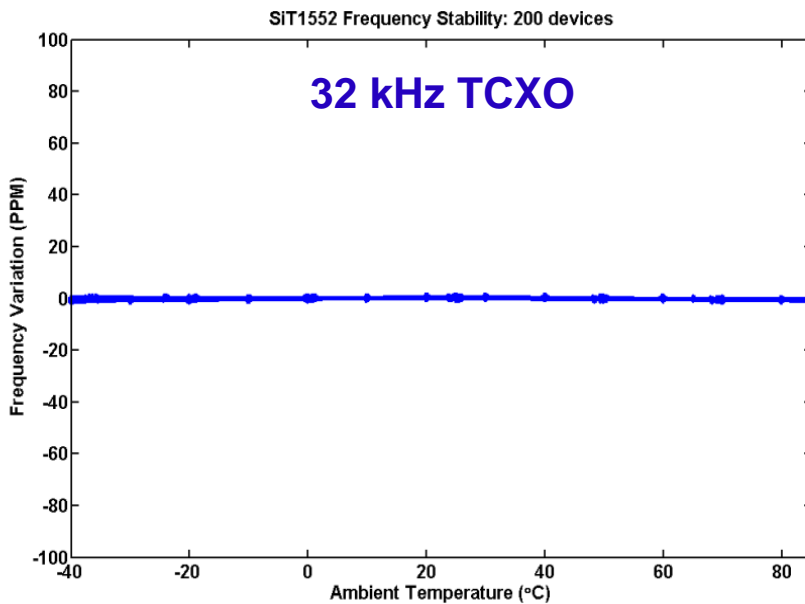
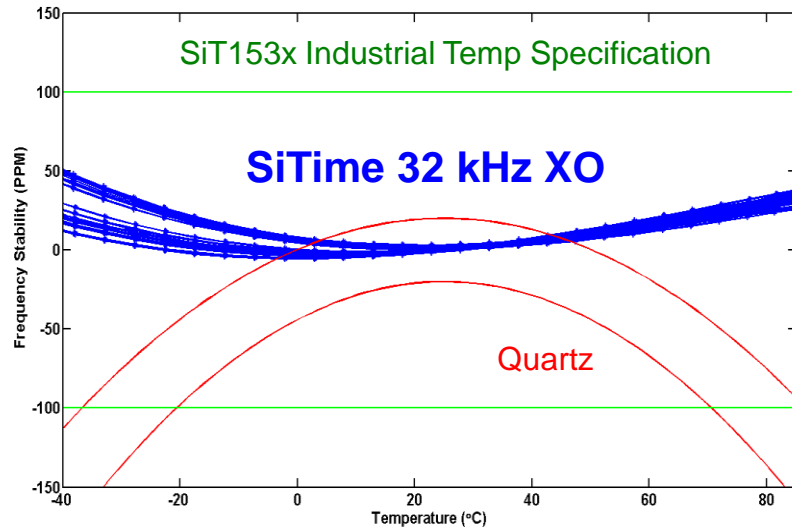
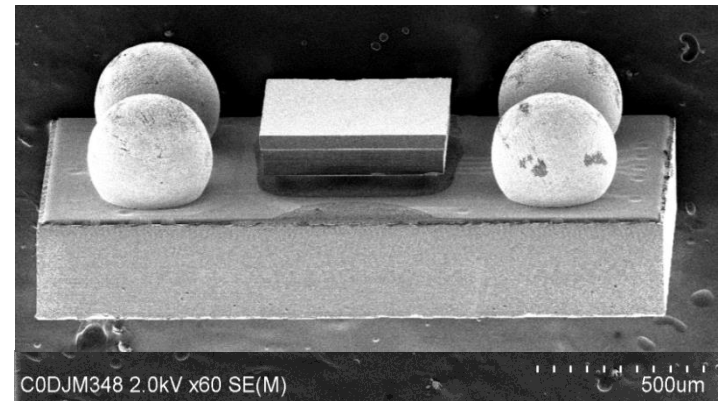
## Measured (30k production units)



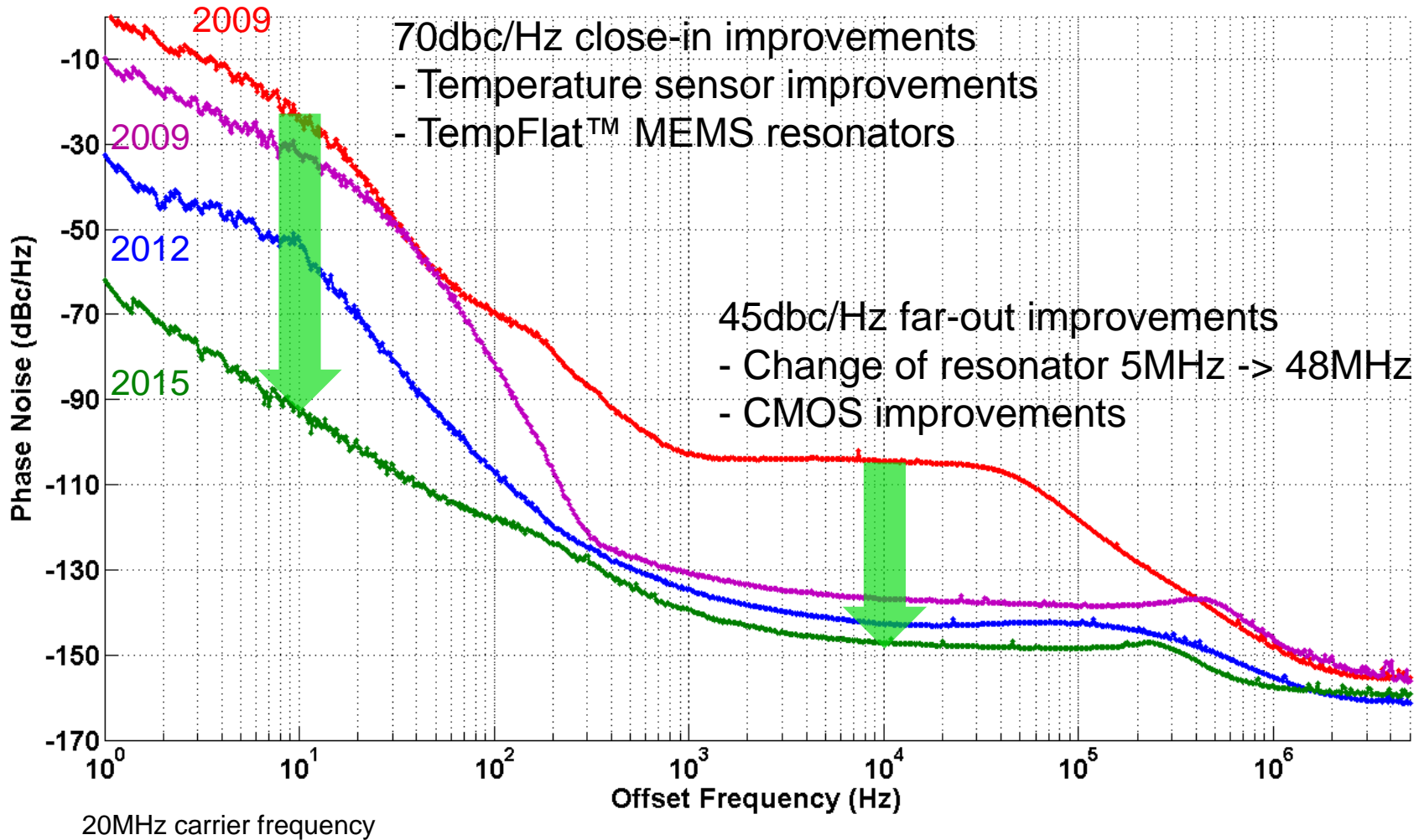
Data of 20 production lots

# 32 kHz XO and TCXO for Wearables, Handsets

- Up to 80% smaller than quartz
- MEMS robustness and reliability
- Production volume 1M/week



# Phase Noise Improvements





# Leading Innovation in Timing Marketplace



**270 Million Units Shipped**

Production for over 7 years  
>80% market share  
100+ major OEMs



**100+ Patents**

First TempFlat™ MEMS  
First nanopower™ oscillator  
World's smallest CSP oscillator



**0.15 DPPM Failure Rate**

100X lower failure rate than quartz  
Zero MEMS failures  
Lifetime warranty

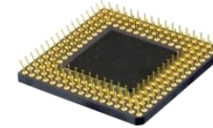
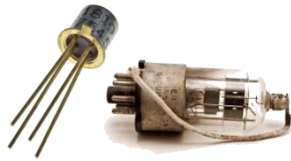


**1 Hz to 800 MHz**

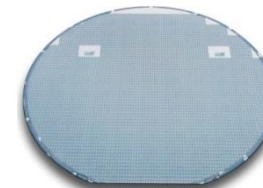
XO, VCXO, DCXO, TCXO  
-50°C to 125°C  
CSP, SOT, 2016, 2520, 3225, 5032, 7050

# Silicon Always Wins

Highest Performance, Best Reliability, Smallest, Lowest Cost



Quartz



**SiTime**<sup>™</sup>  
MEMS

**THANK YOU**